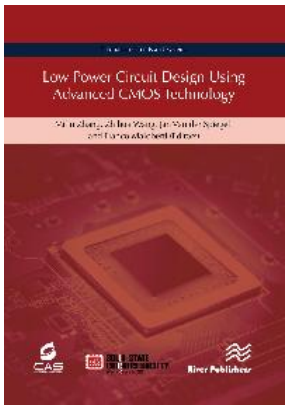

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Low Power Circuit Design Using Advanced CMOS Technology

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Description:

Low Power Circuit Design Using Advanced CMOS Technology is a summary of lectures from the first Advanced CMOS Technology Summer School (ACTS) 2017. The slides are selected from the handouts, while the text was edited according to the lecturers talk.

ACTS is a joint activity supported by the IEEE Circuit and System Society (CASS) and the IEEE Solid-State Circuits Society (SSCS). The goal of the school is to provide society members as well researchers and engineers from industry the opportunity to learn about new emerging areas from leading experts in the field. ACTS is an example of high-level continuous education for junior engineers, teachers in academe, and students. ACTS was the results of a successful collaboration between societies, the local chapter leaders, and industry leaders. This summer school was the brainchild of Dr. Zhihua Wang, with strong support from volunteers from both the IEEE SSCS and CASS. In addition, the local companies, Synopsys China and Beijing IC Park, provided support.

This first ACTS was held in the summer 2017 in Beijing. The lectures were given by academic researchers and industry experts, who presented each 6-hour long lectures on topics covering process technology, EDA skill, and circuit and layout design skills. The school was hosted and organized by the CASS Beijing Chapter, SSCS Beijing Chapter, and SSCS Tsinghua Student Chapter. The co-chairs of the first ACTS were Dr. Milin Zhang, Dr. Hanjun Jiang and Dr. Liyuan Liu. The first ACTS was a great success as illustrated by the many participants from all over China as well as by the publicity it has been received in various media outlets, including Xinhua News, one of the most popular news channels in China.

Keywords: Analog Front-End Design, Mobile and Multimedia SoC, Low Power Digital Design, Mobile Computing, Mobile Embedded, CMOS Wireless Link, Transceiver Design, Layout Design, Advanced Technologies