# Analog IC Design Techniques for Nanopower Biomedical Signal Processing

Authors:

Chutham Sawigun, Mahanakorn University of Technology, Thailand

Wouter A. Serdijn, Delft University of Technology, The Netherlands

As the requirements for low power consumption and very small physical dimensions in portable, wearable and implantable medical devices are calling for integrated circuit design techniques using MOSFETs operating in the subthreshold regime, this book first revisits some well-known circuit techniques that use CMOS devices biased in subthreshold in order to establish nanopower integrated circuit designs. Based on the these findings, this book shows the development of a class-AB current-mode sample-and-hold circuit with an order of magnitude improvement in its figure of merit compared to other state-of-the-art designs. Also, the concepts and design procedures of 1) single-branch filters 2) follower-integrator-based lowpass filters and 3) modular transconductance reduction techniques for very low frequency filters are presented. Finally, to serve the requirement of a very large signal swing in an energy-based action potential detector, a nanopower class-AB current-mode analog multiplier is designed to handle input current amplitudes of more than 10 times the bias current of the multiplier circuit. The invented filter circuits have been fabricated in a standard 0.18 μ CMOS process in order to verify our circuit concepts and design procedures. Their experimental results are reported.
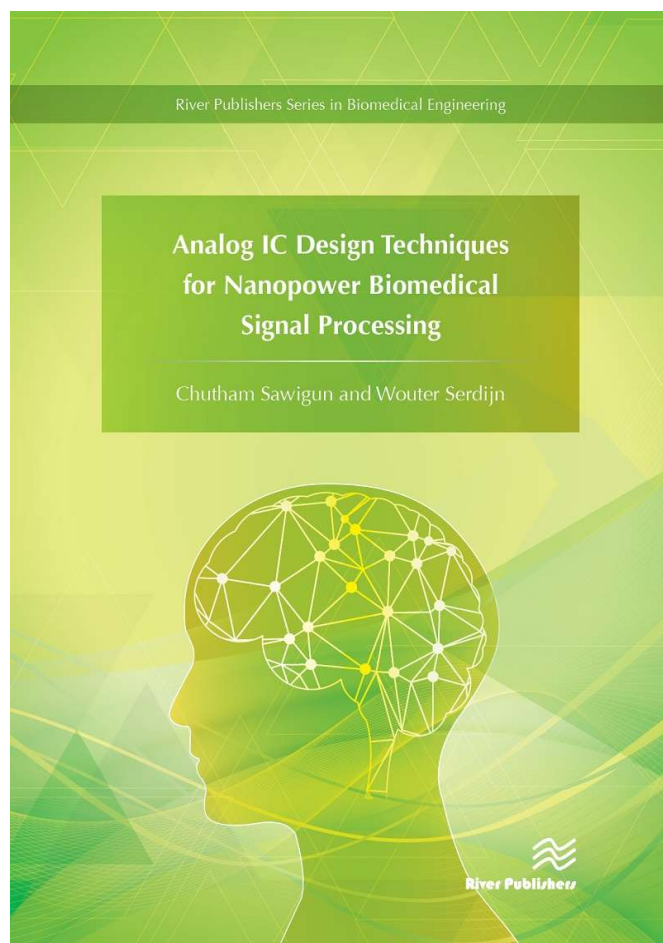
River Publishers Series in Biomedical Engineering

KEYWORDS:
Analog integrated circuit, Biomedical electronics, Bionic ear, Bio-potential, CMOS, Current-mode, Cochlear implant, ECG, Filter, Gm-C, Multiplier, Neural recording, Sample-and-hold, Signal processing, Subthreshold, Switched-current, Transconductance reduction, Transconductor, Weak inversion

www.riverpublishers.com
marketing@riverpublishers.com