LCL Topology Based Single Stage Boost Rectifier Topology for Wireless EV Charging

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Received 04 June 2018; Accepted 26 November 2018; Publication 12 December 2018

Abstract
Research into Wireless power transfer, its applications and adoption is currently growing at a rapid rate. Wireless Power Transfer (WPT) provides a lot of advantages and serves as a solution to the drawbacks of wired power transfer techniques. Research nowadays is focused on developing an inductive power transfer module for transferring power wirelessly. In order to improve the efficiency and the range of power transfer, new coil structures and design schemes for wireless power transfer have been introduced. This paper provides a comparative study of a WPT circuit with a boost rectifier circuit and another with a single-stage boost rectifier circuit, with focus on reducing switching losses and complexity. The DC side inductor and switch of the boost circuit is replaced with an AC side inductor to overcome the disadvantages of boost circuit. For each circuit explained in this paper, Matlab simulation results of output voltage, current and efficiency are analyzed and presented.

Keywords: Wireless power charging, Electric vehicle (EV), Inductive power transfer (IPT), Coupled coils.

doi: 10.13052/jge1904-4720.845
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List of Abbreviations

EV     Electric Vehicle
IPT    Inductive Power Transfer
SIPT   Static Inductive Power Transfer
DIPT   Dynamic Inductive Power Transfer
DC     Direct Current
AC     Alternating Current

1 Introduction

The world is facing major problems today, such as global warming due to high carbon emissions, climate variations, increased temperatures, pollution etc. and most of them point to the use of fossil fuels for energy production and transport as the root cause. Transport is a huge global industry. In order to gradually reduce and eliminate the carbon footprint from the Transport industry, Electric Vehicles (EVs) are being promoted as a viable, clean and safe alternative for transport [1].

Electric Vehicles (EVs) run on charged batteries and battery design is complex in nature, involving identification of battery specifications for various parameters such as energy density, power density, weight and cost. Among the existing battery technologies, Lithium-ion batteries are better suited and are hence preferred for EVs. As of today, EV battery charging is mainly done through a connected medium. It consumes a lot of time and providing a well-connected charging infrastructure for EVs requires a lot of capital investment and maintenance.

Wireless charging, on the other hand, has many advantages over wired charging. It has the potential to simplify and solve the charging infrastructure issues to a large extent. Wireless charging has therefore garnered a lot of interest and is attracting a lot of research in various areas of applications including transport. Inductive Power Transfer (IPT) for EVs is a rapidly evolving technology meant to charge the EV battery using stationary charging (for a parked vehicle), opportunity charging (for vehicles that have temporary stopped, for example, at traffic signals or bus stops), both of these can be categorized into Static charging and the other is dynamic charging (when the vehicle is in motion on a dedicated lane equipped with an IPT system) [16, 17]. Inductive power transfer (IPT) being a wireless power transfer system is a promising futuristic technology with applications in various fields like
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There are no wired connections between the primary and secondary in the IPT model [7–9]. IPT systems can thus be divided into two main types: Static IPT system (SIPT) and Dynamic IPT system (DIPT). In a SIPT system the vehicle is charged at the parking station itself. The main disadvantage of this system is in maintaining the availability of sufficient parking space for the duration of charging. In a DIPT system, the EV is charged while in motion, through a charging station present underground. The dynamic IPT system is the preferred technique due to better efficiency.

To deploy WPT in EV, the power is supplied by the primary coil (transmitter), which is part of the charging unit, while the secondary side (receiver) is present under the EV. The DC power at the supply side is inverted to AC, and using a coupling circuit, the power is supplied to the load. The AC power at the receiver coil is rectified to obtain a DC voltage, which is then used to charge the battery [10, 11]. The EV also has compensation networks that act as a band pass filter to reduce all noise frequency components.

This paper aims to introduce a comparative study of three circuits: double secondary circuit, single secondary circuit [14, 15] and the proposed single stage boost rectifier circuit. The efficiency and loss components are also calculated and analyzed. The Matlab simulation results and hardware implementation details of the circuits are presented and analyzed in Section 4, 5 and 6.

2 System Explanation

Inductive power transfer (IPT) system is modeled to transfer power wirelessly from the primary source to the secondary load side through electromagnetic induction process. The IPT system at the primary side contains an inverter circuit which is accompanied by a primary coupled coil and secondary coil where in the secondary coil transfers power to the load side. On the secondary side, the AC voltage is then rectified to DC and is followed by a boost converter to increase the voltage.

In [1] the IPT system has one primary and two secondary coils. The power transfer is made through the two secondary coils to the load as shown in Figure 1.

The circuit shown in Figure 1 has disadvantages such as increased switching losses, complexity and more number of switches. So to overcome this
disadvantage a topology with single secondary side is considered. The boost circuit used in Figures 1 and 2 also contributes to switching losses. In order to reduce switching losses in the boost circuit, a single stage boost rectifier circuit is proposed as shown in Figure 3. The boost circuit is replaced by an inductor in the secondary coupling side as shown in Figure 3 and operated as single stage boost rectifier.
Figure 3  LCL primary and single stage boost rectifier C secondary topology.

The MOSFETs utilized in the circuits have inbuilt snubber capacitance, thus eliminating the need for an external capacitance in the secondary side. Figure 4 shows a topology with a single stage boost rectifier circuit, excluding the external capacitance in Figure 3, thus forming the proposed circuit for Wireless Power Transfer.

3 Design and Operation

The working of the proposed circuit shown in Figure 4 can be spilt into two parts: a Resonant DC-AC stage and a Boost Rectifier stage.

Figure 4  LCL primary and single stage boost rectifier secondary topology.
3.1 Resonant DC-AC Inverter at the Primary Side

The primary coil uses a resonant switched mode converter (LCL circuit) which exhibits a resonant behavior, resulting in a sinusoidal output. Theoretically the resonant converter has an efficiency of 100%. But in practical use, due to switching and ohmic losses, the efficiency obtained is around 95–98%. The resonant DC-AC network at the primary side is shown in Figure 5.

Modes of operation

The resonant DC-AC inverter circuit illustrated in Figure 5 operates in four modes.

Mode I: Switches S1 and S4 are switched ON. The flow of current in the closed path created is as shown in Figure 6(a). A positive voltage and current is obtained at the load side.

Mode II: Switches S1 and S4 are switched OFF. The diode SD3 and SD2 operates in freewheeling mode and the current flows from load to source as shown in Figure 6(b).

Mode III: This mode is similar to mode I. Switches S2 and S3 are switched ON. The direction of current and the voltage is negative. The flow of negative current in the load is shown in Figure 6(c).

Mode IV: This mode is similar to mode II. Switches S2 and S3 are switched OFF. The diodes SD4 and SD1 freewheels the current to source as shown in Figure 6(d).
3.2 Boost Rectifier at the Secondary Side

The secondary side consists of a single stage boost rectifier which supplies the DC voltage to the load side. In this topology, the boost switch and diode are removed and a boost inductor is added to the rectifier circuit.

Modes of operation

The boost rectifier circuit illustrated in Figure 7 operates in four modes (shown in Figure 8). The first two modes contribute the positive cycle of the output and second two modes contribute the negative cycle.

Mode I: Switches S5 and S6 are switched ON. The input side inductor L3 is connected in parallel with the source, and hence the inductor charges to supply voltage.

Mode II: Switches S5 and S6 are switched OFF. The supply current now freewheels through diode Da and SD6. The voltage across the supply and inductor appears across the load.

Mode III: This mode is similar to Mode I, except the direction of flow of supply current is in the opposite direction. In this mode, switches S6 and S5 are turned ON and the current flows through S6, SD5, L3 and source.

Mode IV: This mode is similar to Mode II, Switches S5 and S6 are turned OFF. The supply current now freewheels through diode Db and SD5. The voltage across the supply and inductor appears across the load.
Figure 7  Boost rectifier circuit.

Figure 8  Modes of operation at secondary side.
3.3 Design of LCL Filter

Power $P_O = 500$ W

Output voltage $V_O = 200$ V

Output current $I_O = \frac{P_O}{V_O} = 2.5$ A

The resonant frequency range is given by:

$$f_{res} \geq 2f_{sw}$$

Therefore, Resonant frequency $f_{res} = 100$ kHz

$$\omega_{res} = 2\pi f_{res} = 200000\pi$$

Switching frequency $f_{sw} = 50$ kHz

$$\omega_g = \omega_{sw} = 2\pi f_{sw} = 100000\pi$$

For maximum power transfer,

$$\text{Base Z} = \frac{V_o}{I_o} = 80 \ \Omega$$

The base capacitance ($C_b$) is expressed as:

$$C_b = \frac{1}{\omega_g Z_b} = 397.8 \ \mu F$$

The rated current

$$I_{max} = \frac{\sqrt{2} P_o}{V_o} = 3.53 \ A$$

A 10% ripple in the rated current for the design parameters can be expressed as:

$$\Delta I_{max} = 0.1 I_{max} = 0.353 \ A$$

The primary capacitance $C_1 < 0.05 \ C_b$

Therefore $C_1 = 0.01 \ C_b = 3.978 \ \mu F$

The resonant inductance $L_2$ is given as,

$$L_2 = \frac{1}{C_1 \omega_{sw}^2} = 2.55 \ \mu H$$

The primary inductance is calculated as,

$$\omega_{res} = \sqrt{\frac{L_1 + L_2}{L_1 L_2 C_f}}$$

Therefore the primary inductor $L_1 = 0.6 \ \mu F$
4 Simulation Results

The proposed WPT topology is simulated in Matlab software. The power is transmitted through the coupled coil to the load. The simulation is carried out for all the four circuits. The first circuit (Figure 1) consists of a wireless system with two secondary coils, the second (Figure 2) has a single secondary, the third (Figure 3) has a single stage boost rectifier topology and the fourth one (Figure 4) is the proposed circuit with single stage boost rectifier and MOSFET internal capacitance.

The simulation parameters used for simulation are listed in Table 1.

Figure 9 illustrates the simulation circuit of the wireless power system with two secondary coils. The output voltage and current for this circuit is given in Figure 10. Figures 11 and 12 shows the simulation diagram of the inductive power system with a single secondary and the output voltage and current of the respective simulations.

Figure 13 exhibits the simulation of a single stage boost rectifier. The DC side inductor is replaced with an AC side inductor on the secondary coil side, which excludes the need for a separate boost circuit; thus eliminating switching losses in boost switch and diode. Figure 14 exhibits the output voltage and current of the boost rectifier circuit. In the proposed topology (Figure 15), the external capacitance in the secondary side is removed in the single stage boost rectifier circuit. Figure 16 exhibits the output voltage and current waveforms for the proposed topology.

Figures 17 and 18 illustrate the current and voltage waveforms across the storage elements. From the waveforms shown in Figures 10, 12, 14 and 16, the output voltage and current is found to be 400 V and 2 A respectively for all

<table>
<thead>
<tr>
<th>Table 1</th>
<th>Simulation parameters</th>
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<tr>
<td></td>
<td>IPT with Two Secondary Coils</td>
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<tr>
<td>$L_1$</td>
<td>9.10 $\mu$F</td>
</tr>
<tr>
<td>$L_2$</td>
<td>1 $\mu$F</td>
</tr>
<tr>
<td>$C_1$</td>
<td>1 $\mu$F</td>
</tr>
<tr>
<td>$C_3$</td>
<td>1.5 $\mu$F (two secondary coils)</td>
</tr>
<tr>
<td>$L_3$</td>
<td>Not applicable</td>
</tr>
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</table>
Figure 9  Simulation of wireless power system with two secondary coils.

Figure 10  Output voltage and current with two secondary coils.
Figure 11  Simulation of inductive power transfer with single secondary.

Figure 12  Output voltage and current with single secondary.
Figure 13  Simulation of inductive power transfer with single stage boost rectifier circuit.

Figure 14  Output voltage and current with boost side rectifier circuit.
Figure 15  Simulation of inductive power transfer with single stage boost rectifier and MOSFET’s internal capacitance.

Figure 16  Output voltage and current for single stage boost rectifier and MOSFET’s internal capacitance.
Figure 17  Current at the inductor L1, voltage across capacitor C1 and pulses at switch S1.

Figure 18  Current at the inductor L3, current across inductor L2 and pulses at switch S1.
the four simulated topologies. The settling time for the circuits is 0.0045 sec, 0.004 sec, 0.0025 sec and 0.002 sec respectively. Since the setting time for the proposed inductive power transfer circuit is lower than that of the other three topologies, it is determined that it can charge the EV battery at a faster rate and lesser time.

5 Efficiency Calculations

At resonant frequency condition, the magnetic energy would be equal to electrical energy, which is defined in terms of amplification coefficient \( \lambda = \frac{I_P}{I_r} \).

\[
\frac{1}{2} C_{P1} V_{C_P}^2 = \frac{1}{2} L_r I_r^2 \quad (9)
\]

\[
\frac{1}{2} C_{P2} V_{C_P}^2 = \frac{1}{2} L_{eq} I_P^2 \quad (10)
\]

Where \( V_{C_P} \) is the rms voltage of capacitance \( C_P \) and \( C_{P1}, C_{P2} \) are parallel capacitance representing \( C_P \).

\[
\lambda = \frac{I_P}{I_r} = \sqrt{\frac{L_r C_{P2}}{L_{eq} C_{P1}}} \quad (11)
\]

For \( \lambda = 1 \) the corresponding \( L_r \) value obtained from Equation (10) is 9.10 \( \mu \)F. With the resulting voltage and current, the efficiency can be calculated as,

\[
\eta = \frac{P_{out}}{V_{in} I_{in}} \quad (12)
\]

The \( \lambda \) is varied from 1 to 5 in Equation (10) with the variation of load resistance from 20 \( \Omega \) to 200 \( \Omega \). From the graph it is to be apparent that as \( \lambda \) increases the efficiency increases with respect to the increase in load.

In the circuit with two secondary coils (Figure 1), with \( \lambda = 1 \) the efficiency achieved is 65% as shown in Figure 19. While the efficiency of single secondary circuit (Figure 2), single stage boost circuit (Figure 3) and single stage boost with MOSFET internal capacitance circuit (Figure 4) are 70%, 74% and 76% at \( \lambda = 1 \) as shown in Figures 20, 21 and 22. The efficiencies for different \( \lambda \) values are listed in Table 2 for comparison.

Analyzing all the efficiency curves and the comparisons in Table 2, it is clear that the single stage boost rectifier without capacitor in secondary is preferable in terms of efficiency when compared to other topologies.
**Figure 19** Efficiency curve of circuit with two secondary coils.

**Figure 20** Efficiency curve of circuit with single secondary.
Figure 21  Efficiency curve of circuit with single stage boost rectifier circuit.

Figure 22  Efficiency curve of circuit with single stage boost and MOSFET internal capacitance.
Table 2  Efficiency and inductance comparison for various $\lambda$

<table>
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<tr>
<th>$\lambda$</th>
<th>$\eta_1$</th>
<th>$L_{r1}$</th>
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<td>1</td>
<td>65%</td>
<td>9.10 $\mu$F</td>
<td>70%</td>
<td>9.10 $\mu$F</td>
<td>74%</td>
<td>9.10 $\mu$F</td>
<td>76%</td>
<td>9.10 $\mu$F</td>
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<tr>
<td>5</td>
<td>93%</td>
<td>249.75 $\mu$F</td>
<td>93%</td>
<td>249.75 $\mu$F</td>
<td>94%</td>
<td>249.75 $\mu$F</td>
<td>94%</td>
<td>249.75 $\mu$F</td>
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6 Comparison of Results

The comparison of all the four topologies are shown in Table 3. The comparison results prove that the single stage boost with MOSFET internal capacitance...
have a lower voltage and current stress, conduction loss compared to other topologies.

From Table 3 it is clear that the proposed circuit has less number of switches and diodes than the IPT with two [1] and one secondary topologies [14, 15]. The proposed circuit has one capacitor less than the IPT with single stage boost rectifier circuit. The voltage and current through the switches and diodes are comparatively lower in the proposed circuit, which evidently means that the voltage and current stress is also less in the proposed circuit. While comparing the settling time of the output voltage in the four circuits, the proposed circuit again has the least value (2 msec). Also, the proposed circuit has an efficiency of 94% at $\lambda = 5$ and 76% at $\lambda = 1$, which is the highest among the topologies discussed. Therefore, by analyzing Table 3, it is evident that the proposed network can charge an EV faster with higher efficiency than the other three topologies.

7 Hardware Results

The simulated proposed topology was implemented in hardware in the laboratory with a step-by-step approach. The complete prototype was realized by integrating a resonant DC/AC converter, transmitter coil, receiver coil, and single stage boost rectifier circuit. The wireless coil consists of a transmitter and receiver coil. The receiver coil is wound in such a way so as to maximize the power transferred to the load side. The transmitter coil consists of 23 turns and the receiver consists of 30 turns. The Figure 23(a) illustrates the source voltage without filter and Figure 23(b) represent source voltage with filter. The Figure 24(b) given below represents the hardware implementation of the Inverter circuit.

![Figure 23](image-url)  
**Figure 23** Output voltage of the diode bridge rectifier (a) without filter (b) with filter.
The single phase inverter is operated with 100 kHz pulses generated from Arduino Uno as shown in Figure 24(a). The resonant converter (DC/AC) in the transmitter side shapes the current waveform as a sinusoidal wave at the transmitter side. MOSFET IRF640 (which has tolerance till 500 kHz, 100 V, 18 A) are used as switches. The input for the inverter is 30 V DC, as shown in Figures 23 and 24 respectively. The output obtained at the load side is 132 V (Vp-p). Figure 25(a) and Figure 25(b) represent the output voltage across transmitter and receiver coil. The peak to peak voltage obtained is 130 V and 120 V in both the cases for 30 V DC input.

8 Conclusion
The inductive power transfer system is designed and simulated in various configurations such as double secondary, single secondary, single secondary
with single stage boost rectifier circuit, single stage boost rectifier circuit without capacitance in secondary. The simulated and hardware results of output voltage and current for all four topologies are analyzed and presented. It is verified that boost rectifier circuit without capacitance in secondary has the least device count (switches, diodes and capacitors). The proposed topology has lower voltage and current stress across the switches, higher efficiency, lower switching losses, less settling time and is simpler in design and hence is more suited for wireless EV battery charging applications.

References


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